



**KALYANI CHARITABLE TRUST'S
LATE G. N. SAPKAL COLLEGE OF ENGINEERING**

Kalyani Hills, Anjaneri-Vadholi, Trimbakeshwar Road, Dist: Nashik - 422 212 (India)
Tel: +91 - 2594 - 220168/71. Fax: +91 - 2594 - 220174
Website: www.sapkalknowledgehub.org, E-mail: gns_engineering@sapkalknowledgehub.com



Ref: KCT's /LGNSCOE/2022-23/ 609-B

Date: 03/05/2023

Notice

The students of T.E. & B.E. E &TC department are hereby informed that, 30 hours Add ON course on "Embedded Systems Design" is organized by Electronics & Telecommunication Engineering Department in association with IQAC at our college.

The students are hereby informed to enroll their names to Prof. K. J. Mahajan on or before 11th May 2023.

It is mandatory to attend all the session of the program.

Resource Person: Prof. (Dr.) Rana S. Mahajan

Venue: Embedded Systems Lab

Day and Date: Monday, 15th May to 19th May 2023

Note: All are advised to refer for the schedule of the program.

Prof. K. J. Mahajan
Program Coordinator

Prof. Dr. V. A. Kolhe
IQAC Coordinator

Prof. S. B. Borse
HOD, E & TC Engg.

Prof. (Dr.) S. B. Bagal
Principal





Kalyanii Charitable Trust's

LATE G. N. SAPKAL COLLEGE OF ENGINEERING

(Accredited with Grade 'B' by NAAC)

Affiliated to > Savitribai Phule Pune University (ID. No.PU/NA/Engg./152/2009 Ref.No.-CA/6501 Dated- 18/11/2009)

Approved by > A.I.C.T.E., New Delhi (F.N: 06/07/MS-Engg/2008/O-17, Dated- 11th June 2009)

> Govt. of Maharashtra (No. GEC-2009/(67/09)/T.E.- 4, Dated- 15th June 2009)

> D.T.E., M.S., Mumbai (No.2/NGC/Engg./Approval/2009/535, Dated - 23rd July 2009)

> AISHE CODE : C-42196

Dr. Sahebrao B. Bagal
M.E. (E & TC), Ph.D. (E & TC)
Principal



Dr. Ravindra G. Sapkal
Chairman & Managing Director
Kalyanii Charitable Trust

Ref: KCT's /LGNSCOE/2022-23/ 615 - B

Date: 09/05/2023

To,
Prof. (Dr.) Rana S. Mahajan
Assistant Professor, E & TC Department
S.V.I.T Chincholi,
Nashik.

Subject: - Invitation letter to conduct Add On Program on "Embedded Systems Design."

Dear Sir,


We are pleased to invite you as a resource person for Add On program on "Embedded Systems Design" organized by Electronics & Telecommunication Engineering Department in association with IQAC of Late G. N. Sapkal College of Engineering, Anjaneri, Nashik 422212.


We will be grateful if you could share your valuable expertise on the topic aforementioned. We hope that the students will be benefitted by participating in this program with the current edge technology on the topic.


We look forward to your presence in the campus as per the schedule attached.


Thanking You,
With warm regards.

Yours Faithfully,


Prof. K. J. Mahajan
Program Coordinator


Prof. Dr. V. A. Kolhe
IQAC Coordinator


Prof. S. B. Borse
HOD, E & TC Engg.


Prof. (Dr.) S. B. Bagal
Principal



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Tel.: + 91- 2594 - 220168/69/70 | Mob.: +91 9922252699 | Toll Free No.: 1800 233 2999 | E-mail : gns_engineering@sapkalknowledgehub.org
- **CORPORATE OFFICE** : Sapkal Knowledge Hub, 'Parag' 46, Ashwin Sector, Opp. Hotel Sai Palace, Mumbai-Agra Highway, Nashik - 422 009.
Tel.: +91 - 253 - 2392450 / 51 | E-mail : head.marketing@sapkalknowledgehub.org | Website : www.sapkalknowledgehub.org
- **MUMBAI OFFICE** : Sapkal Knowledge Hub, Unit No. 22, 1st Floor, Shubhada Tower Shopping Centre, Sir Pochkhanwala Road, Near R.T.O. Office, Worli, Mumbai - 400 030. Tel.: + 91 - 22 - 24938914 / 15 | E-mail : cmd@sapkalknowledgehub.org, ravi.sapkal@gmail.com



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Department of Electronics and Telecommunication
Add On Program
On

Embedded Systems Design
In Association with IQAC Cell, LGNSCOE Nashik
15th to 19th May 2023
Session wise Schedule

Sr. No.	Session	Details
Monday 15/05/2023	Session I (9:00 am to 12:00 pm)	Inauguration of Program Emerging Trends in Embedded System Design
	Session II (1:00 pm to 4:00 pm)	Challenges in Embedded System Design
Tuesday 16/05/2023	Session I (9:00 am to 12:00 pm)	Introduction Arm Cortex-M cores, STM32F4xx Architecture
	Session II (1:00 pm to 4:00 pm)	Hands on Session Interfacing 16 X 2-character LCD display and Keypad with ARM Microcontroller to display the key pressed.
Wednesday 17/05/2023	Session I (9:00 am to 12:00 pm)	Real World Interfacing with Cortex M4 Based Microcontroller
	Session II (1:00 pm to 4:00 pm)	Hands on Session To generate different waveforms using on-chip DAC for LPC 2148.
Thursday 18/05/2023	Session I (9:00 am to 12:00 pm)	Real World Interfacing with STM32XX Based Microcontroller
	Session II (1:00 pm to 4:00 pm)	Hands on Session Embedded C program to Transmit a character from keyboard using on chip UART for STM32F4xx.
Friday 19/05/2023	Session I (9:00 am to 12:00 pm)	Interfacing of real world sensors and standard buses.
	Session II (1:00 pm to 4:00 pm)	Hands on Session Interfacing LDR and MQ3 sensor with STM32F4xx




Department of Electronics & Telecommunication Engineering


Program Module and Outcome


**30 Hrs Certificate Course/Add-on Course on
"Embedded Systems Design"**

Sr No.	Module	Syllabus
1	Module 1	Introduction of Embedded System Design Emerging Trends in Embedded System Design
2	Module 2	Introduction Arm Cortex-M cores, STM32F4xx Architecture
3	Module 3	Real World Interfacing with Cortex M4 and STM32X Based Microcontroller

Sr No.	Module	Outcome
1	Module 1	To enhance the knowledge of students in Embedded System Design
2	Module 2	Ability to understand the importance Arm Cortex-M cores, STM32F4xx Architecture
3	Module 3	Ability to apply the knowledge of Microcontroller to real world interfacing


Prof. K. J. Mahajan
Program Coordinator


Prof. S. B. Borse
HoD E&TC


Prof.(Dr.) S. B. Bagal
Principal





Electronics & Telecommunication Department

PROGRAMME REPORT

- **Title of the Program:** Embedded Systems Design
- **Duration of the Program:** From 15/05/2023 to 19/05/2023
- **Number of Participants:** 90
- **Details of Resource Person:**

Prof. (Dr) Rana S. Mahajan, Mobile No: 9096339024

- **Description of the Program:**

Department of Electronics & Telecommunication Engineering organized a 30 hours Add On program on Embedded Systems Design for Third & Final Year Students. The resource person educated the students with the hardware, software and other resources required for designing an embedded system. Also, he introduced the concepts of Real Time Operating System which is essential in designing an embedded product. He also suggested project ideas in embedded systems that can be developed by the students and the possibilities of getting funds for these projects.

- **Objectives of the Program:**

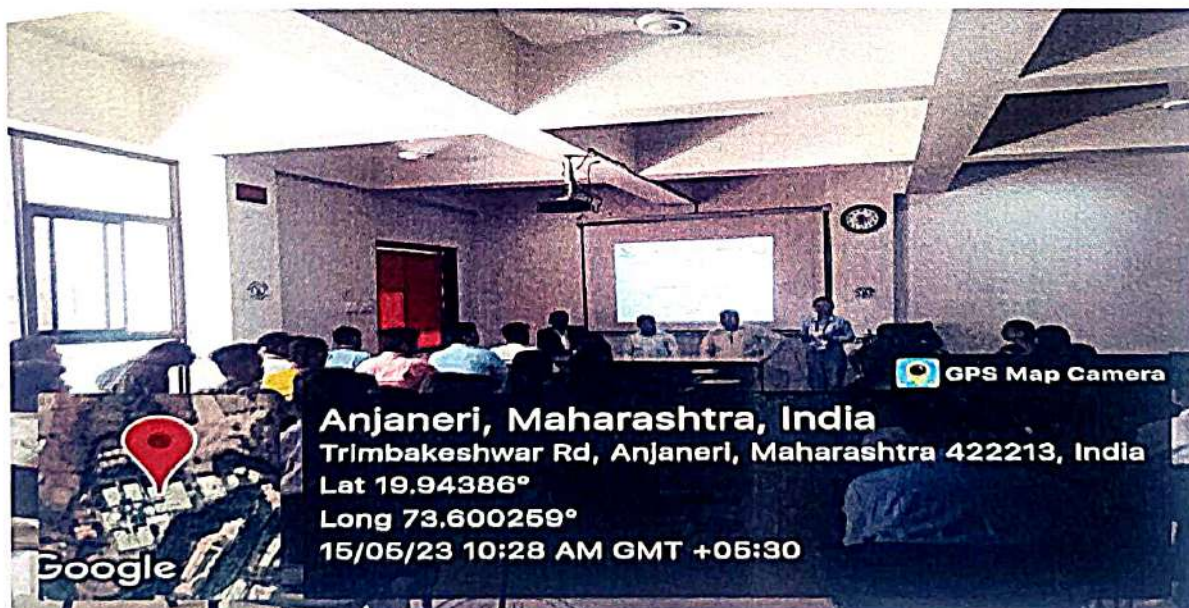
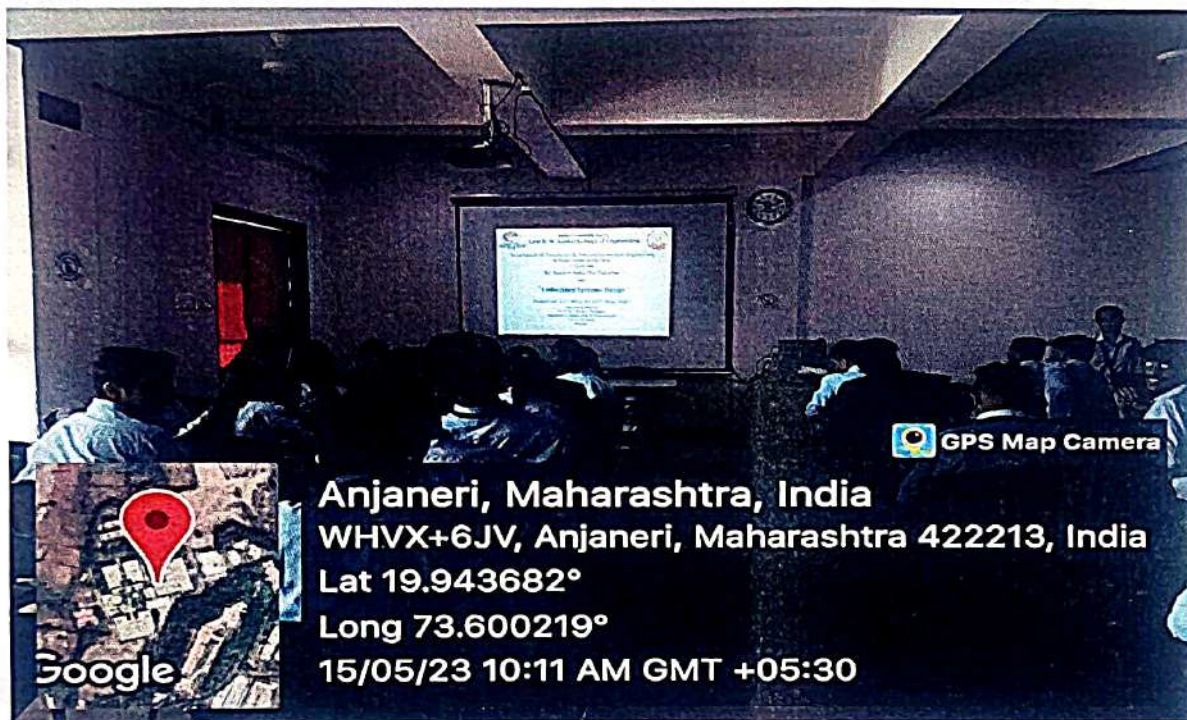
1. To demonstrate the hardware and software used in Embedded Systems Design.
2. To know about the programming of ARM based microcontroller with on chip peripherals and external peripherals.
3. To implement the real world programming of ARM based microcontroller with on chip peripherals and external peripherals.
4. To recognize the interfacing of real world sensors and standard buses.

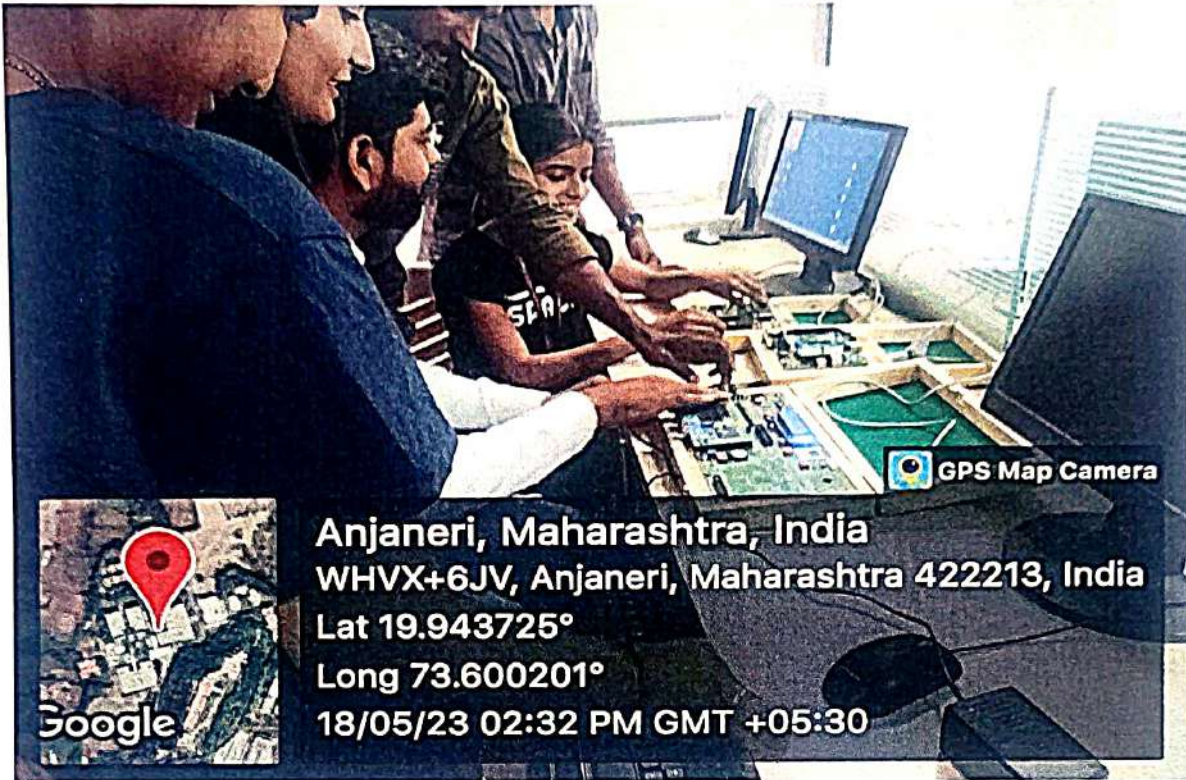


➤ **Outcome of the Program:**

Students gained knowledge on embedded system design with hardware boards. This program covered basic terminologies regarding Embedded System and various ARM architectures. Participants got the architectural and design track right from ARM7/ ARM Cortex- M3& μ COS - II, ARM9 & LINUX to the latest STM32XX Based system.

➤ **Photographs of Event**





Anjaneri, Maharashtra, India
WHVX+6JV, Anjaneri, Maharashtra 422213, India
Lat 19.943725°
Long 73.600201°
18/05/23 02:32 PM GMT +05:30



Google

GPS Map Camera



Anjaneri, Maharashtra, India
WHVX+6JV, Anjaneri, Maharashtra 422213, India
Lat 19.943704°
Long 73.600233°
18/05/23 02:34 PM GMT +05:30



Google


GPS Map Camera







Anjaneri, Maharashtra, India
WHVX+6JV, Anjaneri, Maharashtra 422213, India
Lat 19.943705°
Long 73.600213°
18/05/23 02:36 PM GMT +05:30




Prof. K. J. Mahajan
Program Coordinator


Prof. S. B. Borse
HOD, E&TC Dept.


Prof. (Dr.) S. B. Bagal
Principal, LGNSCOE





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> AISHE CODE : C-42196



Dr. Ravindra G. Sapkal
Chairman & Managing Director
Kalyanii Charitable Trust

Dr. Sahebrao B. Bagal
M.E. (E & TC), Ph.D. (E & TC)
Principal

Ref: KCT's /LGNSCOE/2022-23/

Date: 19/05/2023

To,
Prof. (Dr.) Rana S. Mahajan
Assistant Professor, E & TC Department
S.V.I.T Chincholi,
Nashik.

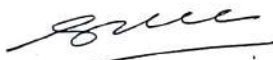
Subject: - Letter of Appreciation.

Dear Sir,


We would like to take this opportunity to express our appreciation to you for successfully conducting 30 hours Add On program "Embedded Systems Design" organized by Department of Electronics & Telecommunication Engineering of Late G. N. Sapkal College of Engineering, Nashik in association with IQAC cell LGNSCOE Nashik, during 15th May 2023 to 19th May 2023.

It was extremely informative, and participation illustrated the importance of this topic to our students. We would like to thank about your personally taking time out of your hectic schedule to provide us with the full of information.

Thanking You,
With warm regards.


Prof. S. B. Borse
HOD E & TC




Prof. (Dr.) S. B. Bagal
Principal



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Department of Electronics & Telecommunication Engineering
in Association with IQAC

ENROLLMENT OF PARTICIPANTS (Students)

30 hours Add On Course on "Embedded Systems Design"

Sr. No.	Name of Participants	Sign
1	Anirudha H. Patil	[Signature]
2	Mina. R. Sabale	[Signature]
3	Neelam. J. Rolcade	[Signature]
4	Shalini. V. Sapnar	[Signature]
5	Rohul Anil Gosavi	[Signature]
6	Avinash. J. Ukude	[Signature]
7	Mayur A. Ahire	[Signature]
8	Vishal. R. Shirsath	[Signature]
9	Bhavesh R. Jadhav	[Signature]
10	Rudraksha Murkute	[Signature]
11	Sanket MURKUTE	[Signature]
12	Saurabh Pawar	[Signature]
13	Harshal Mahajan	[Signature]
14	Dipesh Bhasare	[Signature]
15	Tushar Malede	[Signature]
16	Hinde. muhendrag Jayram	[Signature]
17	Hrishikesh R. Kotwal	[Signature]
18	Shubham. P. Gaikwad	[Signature]
19	Bachhav Utkarsh Sanjay	[Signature]
20	Ashad vijay R.	[Signature]
21	Trite pratibha Anil	[Signature]
22	Wagh Rekha Prakash	[Signature]
23	Kate Harshad Laxman	[Signature]
24	Sawant Sachin Madhabhai	[Signature]
25	Gavit Archana Sherma	[Signature]
26	Gurjal Gayatri Babasaheb	[Signature]
27	Deshmukh Harshal Shrikant	[Signature]
28	Ashad Vaishali Khandu	[Signature]
29	Shivani Sharda Anil	[Signature]
30	Rahul Sanjay Kohe.	[Signature]
31	Gaikwad Kajal Ramesh	[Signature]

32	Pradeep.M. chawla	Pnuel
33	Rohit S Kotare	R Kotare
34	Babade Rutika.V.	Rutika
35	Akash. B. Alhat	Alhat
36	Pendharkar Kalyanee B.	Baljari
37	Jadhav Rukta Prakash	Rukta
38	Pranod shivajirao hivrale	Pranod
39	Idc vijay Bhawa	Vijay
40	Nagare Nishant Bharat	Nishant
41	Arhad Rohit Ramhari	Rohit
42	Rohan Ashok Pardeghi	Rohan
43	Ishwar smnath chaudhuri	Ishwar
44	Dignag Rahul Tylandhe	Rahul
45	Abhishek Ganesh chide	Abhishek
46	Abhishek Milind Kolhe	Abhishek
47	Ahire Prathmesh Pravin	Prathmesh
48	Athare Suyash	Suyash
49	Chaudhary Rohan	Rohan
50	Bhaviskar Ruphesh	Rupesh
51	wagh Rohit	Rohit
52	Lambe Arjune	Arjune
53	Walunj prajakta	Prajakta
54	Katke Anomashree	Anomashree
55	Sengle Pooja	Pooja
56	Kudal Jyoti	Jyoti
57	Khandagale Bhagyashree	Bhagyashree
58	zend Amaya	Amaya
59	Shinde Vanila	Vanila
60	pawar Archana	Archana
61	Sorale Swati	Swati
62	Jadhav Nayan	Nayan
63	Sarap Vikashabh	Vikashabh



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Department of Electronics & Telecommunication Engineering in Association with IQAC

ATTENDANCE OF PARTICIPANTS (Students)

30 hours Add On Course on "Embedded Systems Design"

Sr. No.	Name of Participants	Sign (15 May 2023)	Sign (16 May 2023)	Sign (17 May 2023)	Sign (18 May 2023)	Sign (19 May 2023)
1	Anirudha H. Patil	[Signature]	[Signature]	[Signature]	[Signature]	[Signature]
2	mina. R. sabale	[Signature]	[Signature]	[Signature]	[Signature]	[Signature]
3	Neelam S. Rolcade	[Signature]	[Signature]	[Signature]	[Signature]	[Signature]
4	shalini. v. Sapanar	[Signature]	[Signature]	[Signature]	[Signature]	[Signature]
5	Rohul Anil Gosavi	[Signature]	[Signature]	[Signature]	[Signature]	[Signature]
6	Avinash, J. Ukule	[Signature]	[Signature]	[Signature]	[Signature]	[Signature]
7	Mayur A. Ahire	[Signature]	[Signature]	[Signature]	[Signature]	[Signature]
8	Bhavesh R. Jadhav	[Signature]	[Signature]	[Signature]	[Signature]	[Signature]
9	Vishal. R. Shirsath	[Signature]	[Signature]	[Signature]	[Signature]	[Signature]
10	Rudraksha Murkute	[Signature]	[Signature]	[Signature]	[Signature]	[Signature]
11	Sanket Murkute	[Signature]	[Signature]	[Signature]	[Signature]	[Signature]
12	Saurabh pawar	[Signature]	[Signature]	[Signature]	[Signature]	[Signature]
13	Harshal manajan	[Signature]	[Signature]	[Signature]	[Signature]	[Signature]
14	D. Pesh Bhosale	[Signature]	[Signature]	[Signature]	[Signature]	[Signature]
15	Tushar Malode	[Signature]	[Signature]	[Signature]	[Signature]	[Signature]
16	Hinde mahendraj J.	[Signature]	[Signature]	[Signature]	[Signature]	[Signature]
17	Harshikesh R. Kotwal	[Signature]	[Signature]	[Signature]	[Signature]	[Signature]
18	Shubham .D. Gaikwad	[Signature]	[Signature]	[Signature]	[Signature]	[Signature]
19	Bachhav Uttamresh	[Signature]	[Signature]	[Signature]	[Signature]	[Signature]
20	Arjun vikes	[Signature]	[Signature]	[Signature]	[Signature]	[Signature]
21	Gite pratiksha Anil	[Signature]	[Signature]	[Signature]	[Signature]	[Signature]
22	Wagh Rekha Prakash	[Signature]	[Signature]	[Signature]	[Signature]	[Signature]
23	Kute Harshad Taxman	[Signature]	[Signature]	[Signature]	[Signature]	[Signature]
24	Sawant Balhin Mohobhan	[Signature]	[Signature]	[Signature]	[Signature]	[Signature]
25	Gavit Archana Sherna	[Signature]	[Signature]	[Signature]	[Signature]	[Signature]
26	Anilad Naishali Khandy	[Signature]	[Signature]	[Signature]	[Signature]	[Signature]
27	Gayatri Babasaheb Gunjal	[Signature]	[Signature]	[Signature]	[Signature]	[Signature]
28	Harshal Shrikant Dashmukh	[Signature]	[Signature]	[Signature]	[Signature]	[Signature]
29	Shirani Sharad Anabhale	[Signature]	[Signature]	[Signature]	[Signature]	[Signature]
30	Rahul Sanjay	[Signature]	[Signature]	[Signature]	[Signature]	[Signature]



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31	Gaikwad Kajal R.	Pkew	Pkew	Pkew	Pkew	Pkew
32	Pradeep.M. Chawla	Pkew	Pkew	Pkew	Pkew	Pkew
33	Rohit S Kantare	Pkew	Pkew	Pkew	Pkew	Pkew
34	Barbade Rutika.v.	Pkew	Pkew	Pkew	Pkew	Pkew
35	Pandharkar Kalyanee B.	Balgani	Balgani	Balgani	Balgani	Balgani
36	Akash. B. Amot	J	J	J	J	J
37	Jadhav Yuktal P.	J	J	J	J	J
38	Bamod.s. hirsale	R	R	R	R	R
39	Ide vijay Blima	Pkew	Pkew	Pkew	Pkew	Pkew
40	Ishwar Somnath chaudhni	J	J	J	J	J
41	Nagare Mishant R.	J	J	J	J	J
42	Dignag Rohul Trimbudhe	Pkew	Pkew	Pkew	Pkew	Pkew
43	Abhishek Ganesh chide	Pkew	Pkew	Pkew	Pkew	Pkew
44	Abhishek Milind kolhe	J	J	J	J	J
45	Rohan Ashok Pardehji	Pkew	Pkew	Pkew	Pkew	Pkew
46	Rohit Ramhari Arhad	J	J	J	J	J
47	Ahire Prathmesh Pravin	AP	AP	AP	AP	AP
48	Athare Suyash	SA	SA	SA	SA	SA
49	Chaudhary Rohan	CR	CR	CR	CR	CR
50	Baviskar Rupesh	Rupesh	Rupesh	Rupesh	Rupesh	Rupesh
51	Wagh Rohit	W	W	W	W	W
52	Lambe Arjun	W	W	W	W	W
53	walunj prajakta	W	W	W	W	W
54	Katne Dhanashree	W	W	W	W	W
55	Sagge pooja	Pooja	Pooja	Pooja	Pooja	Pooja
56	Kudal Jyoti	K.A.Jyoti	K.A.Jyoti	K.A.Jyoti	K.A.Jyoti	K.A.Jyoti
57	Chandagale Pragyanshu	P	P	P	P	P
58	Zend Amey	Amey	Amey	Amey	Amey	Amey
59	Shinde Vanila	Vanila	Vanila	Vanila	Vanila	Vanila
60	Pawar Archana	Pawar	Pawar	Pawar	Pawar	Pawar
61	Darale Swati	Swati	Swati	Swati	Swati	Swati
62	Jadhav Nayan	J	J	J	J	J
63	Sanap Vrushabh	Sanap	Sanap	Sanap	Sanap	Sanap



Analysis Report of Program Feedback Form

Title: 30 hours program on Certificate Course "Embedded Systems Design"

Sr. No.	Parameters	Scale→	1	2	3	Sample Size	Average Rating Out of 3
1	Did you find the workshop is useful?	Qty	2	10	21	33	2.58
		Point wise weightage	2	20	63	85	
2	Did it fulfil your expectation?	Qty	2	12	19	33	2.52
		Point wise weightage	2	24	57	83	
3	Did the workshoop improve your knowledge about the topic discussed?	Qty	2	11	20	33	2.55
		Point wise weightage	2	22	60	84	




Asst. Professor & Head
 Dept. of Electronics & Telecom. Engineering
 Late G. N. Sapkal College of Engineering
 Anjaneri, Nashik-422212

Late G. N. Sapkal College of Engineering
Department of Electronics & Telecommunication Engineering
FEEDBACK OF PARTICIPANTS

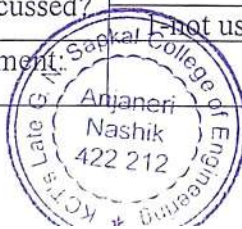
Title of the Program/Event: 30 hours Certificate course on “**Embedded Systems Design**”
Department of Electronics & Telecommunication Engineering
in Association with IQAC

Name of Participant: ...Mayur A. Ahire.....

Name of the Department: Mechanical/Civil/Electrical/E&TC/Computer/Other

1.	Did you find the Workshop is useful?	1	2	3
				✓
		1-not useful ; 2-To some Extend ; 3-Very useful		
2.	Did it fulfill your expectation?	1	2	3
			✓	
		1-not useful ; 2-To some Extend ; 3-Very useful		
3.	Did the Workshop improve your knowledge about the topic discussed?	1	2	3
				✓
		1-not useful ; 2-To some Extend ; 3-Very useful		
3.	Suggestion to further improvement:			

Date: 19/05/2023



ABV
Signature of Participants

Late G. N. Sapkal College of Engineering
Department of Electronics & Telecommunication Engineering
FEEDBACK OF PARTICIPANTS

Title of the Program/Event: 30 hours Certificate course on "Embedded Systems Design"
Department of Electronics & Telecommunication Engineering
in Association with IQAC

Name of Participant: Avinash J. Ukade

Name of the Department: Mechanical/Civil/Electrical/E&TC/Computer/Other

1.	Did you find the Workshop is useful?	1	2	3
				✓
		1-not useful ; 2-To some Extend ; 3-Very useful		
2.	Did it fulfill your expectation?	1	2	3
				✓
		1-not useful ; 2-To some Extend ; 3-Very useful		
3.	Did the Workshop improve your knowledge about the topic discussed?	1	2	3
				✓
		1-not useful ; 2-To some Extend ; 3-Very useful		
3.	Suggestion to further improvement:			

Date: 19/05/2023



A. J. Ukade
Signature of Participants

Late G. N. Sapkal College of Engineering
Department of Electronics & Telecommunication Engineering
FEEDBACK OF PARTICIPANTS

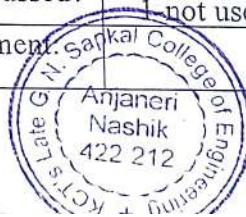
Title of the Program/Event: 30 hours Certificate course on "Embedded Systems Design"
Department of Electronics & Telecommunication Engineering
in Association with IQAC

Name of Participant: ... Rohul Anil Gosavi

Name of the Department: Mechanical/Civil/Electrical/E&TC/Computer/Other

1.	Did you find the Workshop is useful?	1	2	3
				✓
		1-not useful ; 2-To some Extend ; 3-Very useful		
2.	Did it fulfill your expectation?	1	2	3
			✓	
		1-not useful ; 2-To some Extend ; 3-Very useful		
3.	Did the Workshop improve your knowledge about the topic discussed?	1	2	3
				✓
		1-not useful ; 2-To some Extend ; 3-Very useful		
3.	Suggestion to further improvement			

Date: 19/05/2023



Rohul Anil Gosavi
Signature of Participants

Late G. N. Sapkal College of Engineering
Department of Electronics & Telecommunication Engineering
FEEDBACK OF PARTICIPANTS

Title of the Program/Event: 30 hours Certificate course on “**Embedded Systems Design**”
Department of Electronics & Telecommunication Engineering
in Association with IQAC

Name of Participant: Sapnor Shalini Vilay

Name of the Department: Mechanical/Civil/Electrical/E&TC/Computer/Other

1.	Did you find the Workshop is useful?	1	2	3
				✓
1-not useful ; 2-To some Extend ; 3-Very useful				
2.	Did it fulfill your expectation?	1	2	3
				✓
1-not useful ; 2-To some Extend ; 3-Very useful				
3.	Did the Workshop improve your knowledge about the topic discussed?	1	2	3
				✓
1-not useful ; 2-To some Extend ; 3-Very useful				
3.	Suggestion to further improvement:			



Date: 19/05/2023

Sapnor
Signature of Participants



Kalyani Charitable Trust's
**LATE G. N. SAPKAL
COLLEGE OF ENGINEERING**



Sapkal Knowledge Hub, Kalyani Hills, Anjaneri, Trimbakeshwar Road, Nashik.


Department of Electronics & Telecommunication Engineering


CERTIFICATE

This is to certify that Kajal R. Gaikwad has
completed 30 hours Add-on Program on "Embedded Systems Design" organized by Electronics &
Telecommunication Department of Late G. N. Sapkal College of Engineering in Association with IQAC
from May 15, 2023 to May 19, 2023.




Prof. K. J. Mahajan
Program Coordinator


Prof. S. B. Borse
Head of Department


Prof. (Dr.) S. B. Bagal
Principal



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Sapkal Knowledge Hub, Kalyani Hills, Anjaneri, Trimbakeshwar Road, Nashik.


Department of Electronics & Telecommunication Engineering


CERTIFICATE

This is to certify that Kalyanee Bhausaheb Pendharkar has completed 30 hours Add-on Program on "Embedded Systems Design" organized by Electronics & Telecommunication Department of Late G. N. Sapkal College of Engineering in Association with IQAC from May 15, 2023 to May 19, 2023.




Prof. K. J. Mahajan
Program Coordinator


Prof. S. B. Borse
Head of Department


Prof. (Dr.) S. B. Bagal
Principal



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Department of Electronics & Telecommunication Engineering


CERTIFICATE

This is to certify that Mina R. Sabale has
completed 30 hours Add-on Program on "Embedded Systems Design" organized by Electronics &
Telecommunication Department of Late G. N. Sapkal College of Engineering in Association with IQAC
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Prof. K. J. Mahajan
Program Coordinator


Prof. S. B. Borse
Head of Department


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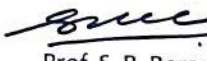
Department of Electronics & Telecommunication Engineering


CERTIFICATE

This is to certify that Anirudha H. Patil has
completed 30 hours Add-on Program on "Embedded Systems Design" organized by Electronics &
Telecommunication Department of Late G. N. Sapkal College of Engineering in Association with IQAC
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Prof. K. J. Mahajan
Program Coordinator


Prof. S. B. Borse
Head of Department


Prof. (Dr.) S. B. Bagal
Principal