



ACAD-R-07A	CLASS TIME TABLE	Academic Year: 2026-27
Rev.: 00		
Date: 01-12-2024		Semester-I

DEPARTMENT OF ELECTRONICS AND TELECOMMUNICATION

CLASS: TE

W. E. F.-01/07/2026

DAY	09:15 TO 10:15	10:15 TO 11:15	11:15 TO 12:00	12:00 TO 01:00	01:00 TO 2:00	02:00 TO 2:10	02:10 TO 03:05	03:05 TO 04:00
	1	2		3	4		5	6
MON	M&A(SSJ)	CC(ADN)	LONG RECESS	OE-III(DAJ)	ML(AAD)	SHORT RECESS	T1,T2,T3-TS(SGB)	
TUE	CC(ADN)	CNW(MGK)		ML(AAD)	VLSI(SKK)		T1-VLSI(SKK) T2-CC(ADN) T3-TG	
WED	M&A(SSJ)	CC(ADN)		VLSI(SKK)	CNW(MGK)		T2-VLSI(SKK) T3-CC(ADN) T1-TG	
THUR	CNWMGK)	M&A(SSJ)		T1-M&A(SSJ) T2-CNW(MGK) T3-ML(AAD)			T3-VLSI(SKK) T1-CC(ADN) T2-TG	
FRI	VLSI(SKK)	OE-III(DAJ)		T2-M&A(SSJ) T3-CNW(MGK) T1-ML(AAD)			T3-M&A(SSJ) T1-CNW(MGK) T2-ML(AAD)	

Name of the Class Coordinator: Prof. A.D.Nimone

Name of the Teacher Guardian (TG)	Batch	Roll No.s	Contact No. of TG
Prof.A.D.Nimone	T1	1 to 20	9870222077
Prof.(Dr.)M.G. Kude	T2	21 to 40	8983497805
Prof. S.S Jagtap	T3	41 to 69	9422703522

Subject Name	Faculty Name	Initials	TH.	Pr./Tut.	Pr./Tut. conduction Lab.
Microcontroller & Application	Prof.S.S.Jagtap	SSJ	1	...	Class Room
Cellular Network	Prof.(Dr.)M.G. Kude	MGK	1	..	Class Room
VLSI Design	Prof.S.K. Khaire	SKK	1	...	Class Room
Microcontroller & Application Lab	Prof.S.S.Jagtap	SSJ	...	2	PG Lab
VLSI Lab	Prof.S.K. Khaire	SKK		2	VLSI Lab
Cellular Network Lab	Prof.(Dr.)M.G. Kude	MGK		2	Digital communication Lab
Cloud Computing	Prof.A.D.Nimone	ADN	1	...	Class Room
Cloud Computing Lab	Prof.A.D.Nimone	ADN		2	PG Lab
Machine Learning	Prof.A.A. Daware	AAD	1	2	Class Room
Open Elective-III	Prof.D.A.Jagtap	DAJ	1		Class Room
Technical Seminar	Prof.S.G. Bagul	SGB		2	Class Room

Prof.(Dr.)M.G.Kude
 Time Table I/C

Prof.S.G.Bagul
 HOD

Prof. (Dr.) S.B. Bagal
 Principal